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APPLICATION N	io. Fi	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/712,236		11/12/2003	Shiao-Shien Chen	JCLA11795 9474	
23900	7590	01/14/2005		EXAMI	NER
	ENTS, INC JRE, SUITE:		HU, SHOUXLANG		
IRVINE, CA 92618				ART UNIT PAPER NUMBER	
				2011	

DATE MAILED: 01/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

,	Application No.	Applicant(s)					
	10/712,236	CHEN ET AL.					
Office Action Summary	Examiner	Art Unit					
	Shouxiang Hu	2811					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tim y within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from t , cause the application to become ABANDONED	ely filed will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on <u>02 November 2004</u> .							
2a)⊠ This action is FINAL . 2b)☐ This	action is non-final.						
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
 4) Claim(s) 1-18 is/are pending in the application. 4a) Of the above claim(s) 1-11 is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 12-18 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement. 							
Application Papers							
9)⊠ The specification is objected to by the Examiner.							
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date							
Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date		te atent Application (PTO-152)					

DETAILED ACTION

Claim Objections

 Claims 12-18 are objected to because of the following informalities: and/or defects:

In claims 12-18, the terms of "with" in "substrate with" and "region with" should read as: --of--.

Claims 12-18 each recite a plurality of diodes, but these diodes should be referred to as: a starting diode, an ending diode and intermediate diode(s), respectively, in order to avoid potential confusions on them.

Claim 13 need to clarify whether the recited "post diode structure" is within the recited "diode string structure"; and which diode the term of "the diode structure refers to.

In claims 14 and 15, the term of "when the diode is" should read as: --for the diode--

Appropriate correction is required.

Claim Rejections - 35 USC § 112

2. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

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3. Claims 12-18 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. Claim 12 recites the subject matters that each diode structure is detached from the first doping region; but they are not fully supported by the original disclosure.

According to Fig. 3 of the instant disclosure, each of the recited diode (303) is formed in and in direct contact to the same first well (302) that has a well contact region, i.e., the recited first region (306). Therefore, each of the diode is naturally and substantially electrically coupled to the first region, instead of being "detached from" it.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 12-18, insofar as being in compliance with 35 U.S.C. 112 and as being best understood in view of the above claim objections, are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art

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("AAPA") in view of Li (TW 390013 A; May 11, 2000) and/or Ohkawa (US 5,798,560; of record).

AAPA (see Fig. 1, and also see Paragraphs 0004 through 0007 of the instant disclosure) disclose the claimed invention, except that AAPA does not expressly disclose that the diode string can be formed with a triple-well structure having a well contact region in the first well at the starting end.

However, one of ordinary skill in the art would readily recognize that such triple-well structure is desirable for eliminating the adverse parasitic transistor effect therein and/or reducing leaking current through the substrate, as evidenced in Li (see the first well (N-Well) and the second well (P-Well) in Figs. 2-5), and/or in Ohkawa (see the first region (41B) and the first well (including 26 and 41b) in the cover page figure), wherein the first region functions as a well contact region for the first well and is connected to a power supply terminal having a potential equal or higher than the anode terminal of the diode structure (see col. 4, lines 60-63).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to make the diode structure of AAPA with a triple-well structure having first region as a well contact region diode at the starting end that has the highest potential, per the teachings of Li and Ohkawa, so that a diode string without the adverse parasitic transistor effect and/or with reduced leaking current would be obtained.

Regarding claim 17, it is noted that the diode structure of AAPA further includes a first STI (107) between the neighboring diodes and a second STI

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(107) between the two electrode contact regions in each diode. And, it would be well within the ordinary skill in the art to form the above collectively taught diode structure with the STI therein being formed between each pair of the neighboring electrode contact regions for improving the separation the neighboring electrode contact regions, as evidenced in the prior art such as Santin et al. (US 5,815,026; see the surface isolation regions between the electrode contact regions 24", 26" and 16" in Fig. 4).

Response to Arguments

6. Applicant's arguments with respect to claims 12-18 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**.

See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be

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calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shouxiang Hu whose telephone number is 571-272-1654. The examiner can normally be reached on Monday through Thursday, 7:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on 571-272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SH January 11, 2005

SHOUXIANG HU PRIMARY EXAMINER